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REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2113

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/882,417
Filed: June 15, 2001
Inventor(s):
Chambers et al.

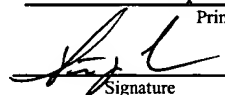
Title: A Circuit and Method For
Correcting Erroneous Data
In Memory For Pipelined
Reads

§ Examiner: Abraham, Esaw T.
§ Group/Art Unit: 2133
§ Atty. Dkt. No: 5500-67700
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Stephen J. Curran

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Signature

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RESPONSE TO OFFICE ACTION OF
DECEMBER 3, 2004

ATTN: BOX AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of December 3, 2004, to further highlight why the application is in condition for allowance.